

## A RF transceiver for WDCT in a 25 GHz Si bipolar technology

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*G. Li Puma, W. Geppert, K. Hadjizada, S. Van Waasen, W. Mevissen, W. Von Schwartzberg and S. Heinen. "A RF transceiver for WDCT in a 25 GHz Si bipolar technology." 2000 Radio Frequency Integrated Circuits (RFIC) Symposium 00. (2000 [RFIC]): 257-261.*

An RF Si bipolar transceiver IC for WDCT is presented. The complete transceiver operates from 3.1 V to 5.1 V and provides very high integration level. The receiver uses a single conversion architecture with an image-reject frontend and needs no external trimming.

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